	Datasheet	No.	DS10-U003
		Initial Date	2017-12-28
OU	OU8S341	Written Team	R&D Dept.
			GH Zheng

I Preview


PN	OU8S341
Description	100G QSFP28 LR4 1310nm-band 10KMLC DDMI 0~70 °C

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2. Applications
3. Description
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III Revision History

No.	Date	Items	Change Recording	Ver.	Rev.	Customer
1	2017-12-28	All	Initial registration	000	000	Standard
2	2018-12-21	All	Update	001	000	Standard
3						
4						
5						
6						

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1. Features

- ◆ Hot Pluggable QSFP28 form factor
- ◆ Four-channel full-duplex transceiver modules
- ◆ Support 103.1Gb/s aggregate bite rate
- ◆ Up to 10km transmission on Single Mode Fiber(SMF)
- ◆ Low power dissipation <3.5W
- ◆ Operating case temperature 0 ℃ to +70 ℃
- ◆ 4x25.78125Gb/s cooled LAN-WDM DFB transmitter (1295.56, 1300.05, 1304.58, 1309.14nm)
- ◆ 3.3V power supply voltage
- ◆ RoHS 6 compliant
- ◆ LC connector receptacle
- ◆ Built-in digital diagnostic function

2. Applications

- ◆ 100GBase-LR4 Ethernet
- ◆ Infiniband QDR DDR and EDR interconnects
- ◆ 100G Telecom connections.
- ◆ Other links

3. Description


The OCRECOM's OU8S341 is a 100Gb/s transceiver module designed for optical communication applications compliant to 100GBASE-LR4 of the IEEE 802.3ba standard. The module converts 4 input channels of 25Gb/s electrical data to 4 channels of LAN WDM optical signals and then multiplexes them into a single channel for 100Gb/s optical transmission. Reversely on the receiver side, the module de-multiplexes a 100Gb/s optical input into 4 channels of LAN WDM optical signals and then converts them to 4 output channels of electrical data.

The central wavelengths of the 4 LAN WDM channels are 1295.56, 1300.05, 1304.58 and 1309.14 nm as members of the LAN WDM wavelength grid defined in IEEE 802.3ba. The high performance cooled LAN WDM DFB transmitters and high sensitivity PIN receivers provide superior performance for 100Gigabit Ethernet applications up to 4km links and compliant to optical interface with 100GBASE-LR4 requirements specified in IEEE 802.3ba Clause 88.

The product is designed with form factor, optical/electrical connection and digital diagnostic interface according to the QSFP+ Multi-Source Agreement (MSA). It has been designed to meet the harshest external operating conditions including temperature, humidity and EMI interference.

The transceiver module receives 4 channels of 25Gb/s electrical data, which are processed by a 4-channel Clock and Data Recovery (CDR) IC that reshapes and reduces the jitter of each electrical signal. Subsequently, DFB laser driver IC converts each one of the 4 channels of electrical signals to an optical signal that is transmitted from one of the 4 cooled DFB lasers which are packaged in the Transmitter Optical Sub-Assembly (TOSA). Each laser launches the optical signal in specific wavelength specified in IEEE 802.3ba 100GBASE-LR4 requirements. These 4-lane optical signals will be

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optically multiplexed into a single fiber by a 4-to-1 optical WDM MUX. The optical output power of each channel is maintained constant by an automatic power control (APC) circuit. The transmitter output can be turned off by TX_DIS hardware signal and/or 2-wire serial interface.

The receiver receives 4-lane LAN WDM optical signals. The optical signals are de-multiplexed by a 1-to-4 optical DEMUX and each of the resulting 4 channels of optical signals is fed into one of the 4 receivers that are packaged into the Receiver Optical Sub-Assembly (ROSA). Each receiver converts the optical signal to an electrical signal. The regenerated electrical signals are retimed and de-jittered and amplified by the RX portion of the 4-channel CDR. The retimed 4-lane output electrical signals are compliant with IEEE CAUI-4 interface requirements. In addition, each received optical signal is monitored by the DOM section. The monitored value is reported through the 2-wire serial interface. If one or more received optical signal is weaker than the threshold level, RX_LOS hardware alarm will be triggered.

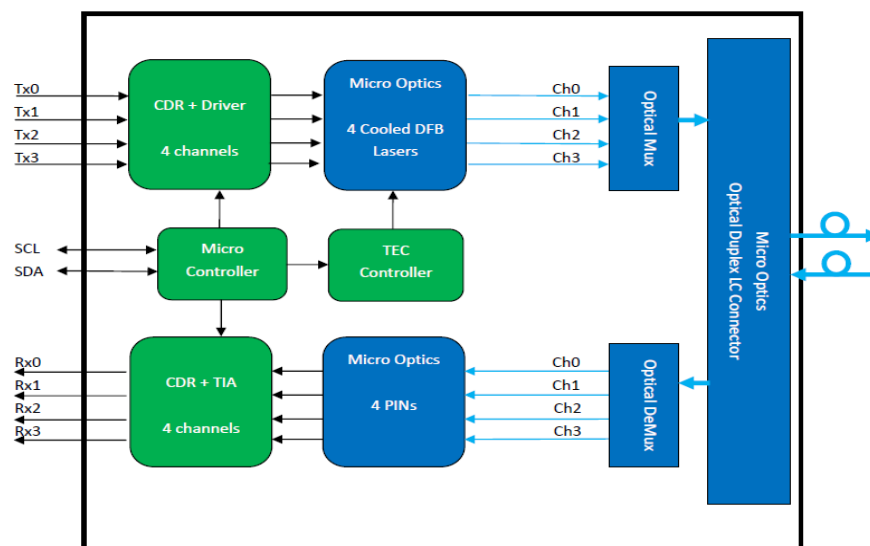


Figure 1 Block Diagram of transceiver

4. Absolute Maximum Ratings


The operation in excess of any absolute maximum ratings might cause permanent damage to this module.

Parameter	Symbol	Min	Max	Unit	Note
Storage Temperature	TST	-40	85	°C	
Relative Humidity(non-condensing)	RH	0	85	%	
Operating Case Temperature	TOPC	0	70	°C	
Supply Voltage	VCC	-0.5	3.6	V	
Damage Threshold, each Lane	TH _d	5.5		dBm	

5. Operating Environment

Parameter	Symbol	Min	Typical	Max	Unit
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Operating Case Temperature	TOPC	0		70	°C
Power Supply Voltage	VCC	3.13	3.3	3.47	V
Power dissipation		-		3.5	W
Data Rate	DR		25.78125		Gbps
Data Speed Tolerance	ΔDR	-100		+100	ppm
Control Input Voltage High		2		Vcc	V
Control Input Voltage Low		0		0.8	V
Link Distance with G.652	D	0.002		10	km

6. Optical Characteristics

All parameters are specified under the recommended operating conditions with PRBS31 data pattern unless otherwise specified.

Transmitter						
Parameter	Symbol	Min	Typical	Max	Unit	Notes
Wavelength Assignment	L0	1294.53	1295.56	1296.59	nm	
	L1	1299.02	1300.05	1301.09	nm	
	L2	1303.54	1304.58	1305.63	nm	
	L3	1308.09	1309.14	1310.19	nm	
Side Mode Suppression Ratio	SMSR	30			dB	
Total Average Launch Power	P _T			10.5	dBm	
Average Launch Power, each lane	P _{AVG}	-6.0	-	+4.5	dBm	
Optical Modulation Amplitude (OMA)	P _{OMA}	-3.0	-	+4.5	dBm	1
Difference in Launch Power between any two lanes	P _{tx,diff}			5	dB	
Launch Power in OMA minus Transmitter and Dispersion Penalty (TDP), each Lane		-4.0			dBm	
Transmitter and Dispersion Penalty per Lane	TDP			2.2	dBm	
Extinction Ratio	ER	4			dB	
RIN _{OMA}	RIN			-130	dB/Hz	
Optical Return Loss Tolerance	TOL			20	dB	
Transmitter Reflectance	RT			-12	dB	
Average Launch Power OFF Transmitter, each Lane	P _{off}			-30	dBm	
Transmitter Eye Mask Definition {X1, X2, X3, Y1, Y2, Y3}		{0.25, 0.4, 0.45, 0.25, 0.28, 0.4}				2

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Receiver						
Wavelength Assignment	L0	1294.53	1295.56	1296.59	nm	
	L1	1299.02	1300.05	1301.09	nm	
	L2	1303.54	1304.58	1305.63	nm	
	L3	1308.09	1309.14	1310.19	nm	
Damage Threshold	THd	5.5			dBm	3
Total Average Receive Power				10.5	dBm	
Average Receive Power, each Lane		-10.6		4.5	dBm	
Receive Power (OMA), each Lane				4.5	dBm	
Receiver Sensitivity in OMA, each Lane	SEN			-8.6	dBm	
Stressed Receiver Sensitivity (OMA), each Lane				-6.5	dBm	4
Receiver Reflectance	R _R			-26	dB	
Difference in Receive Power between any Two Lanes (OMA)	Prx,diff			5.5	dB	
Signal Loss Assert Threshold	LOSA	-30			dBm	
Signal Loss Deassert Threshold	LOSD			-13	dBm	
LOS Hysteresis	LOSH	0.5	-	-	dB	
Receiver Electrical 3 dB upper Cutoff Frequency, each Lane	F _c			31	GHz	
Conditions of Stress Receiver Sensitivity Test						5
Vertical Eye Closure Penalty, each Lane			1.8		dB	
Stressed Eye J2 Jitter, each Lane			0.3		UI	
Stressed Eye J9 Jitter, each Lane			0.47		UI	

Notes:

- 1, Even if the TDP < 1 dB, the OMA min must exceed the minimum value specified here.
- 2, Hit ratio 5x10⁻⁵.
- 3, The receiver shall be able to tolerate, without damage, continuous exposure to a modulated optical input signal having this power level on one lane. The receiver does not have to operate correctly at this input power.
- 4, Measured with conformance test signal at receiver input for BER = 1x10⁻¹².

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5, Vertical eye closure penalty, stressed eye J2 jitter, and stressed eye J9 jitter are test conditions for measuring stressed receiver sensitivity. They are not characteristics of the receiver.

7. Electrical Specifications

Parameter	Test Point	Min	Typical	Max	Units	Notes
Power Consumption				3.5	W	
Supply Current	Icc			1.06	A	
Transmitter (each Lane)						
Overload Differential Voltage pk-pk	TP1a	900			mV	
Common Mode Voltage (Vcm)	TP1	-350		2850	mV	1
Differential Termination Resistance Mismatch	TP1			10	%	At 1MHz
Differential Return Loss (SDD11)	TP1			See CEI-28G-VSR Equation 13-19	dB	
Common Mode to Differential conversion and Differential to Common Mode conversion (SDC11, SCD11)	TP1			See CEI-28G-VSR Equation 13-20	dB	
Stressed Input Test	TP1a	See CEI-28G-VSR Section 13.3.11.2.1				
Receiver (each Lane)						
Differential Voltage, pk-pk	TP4			900	mV	
Common Mode Voltage (Vcm)	TP4	-350		2850	mV	1
Common Mode Noise, RMS	TP4			17.5	mV	
Differential Termination Resistance Mismatch	TP4			10	%	At 1MHz

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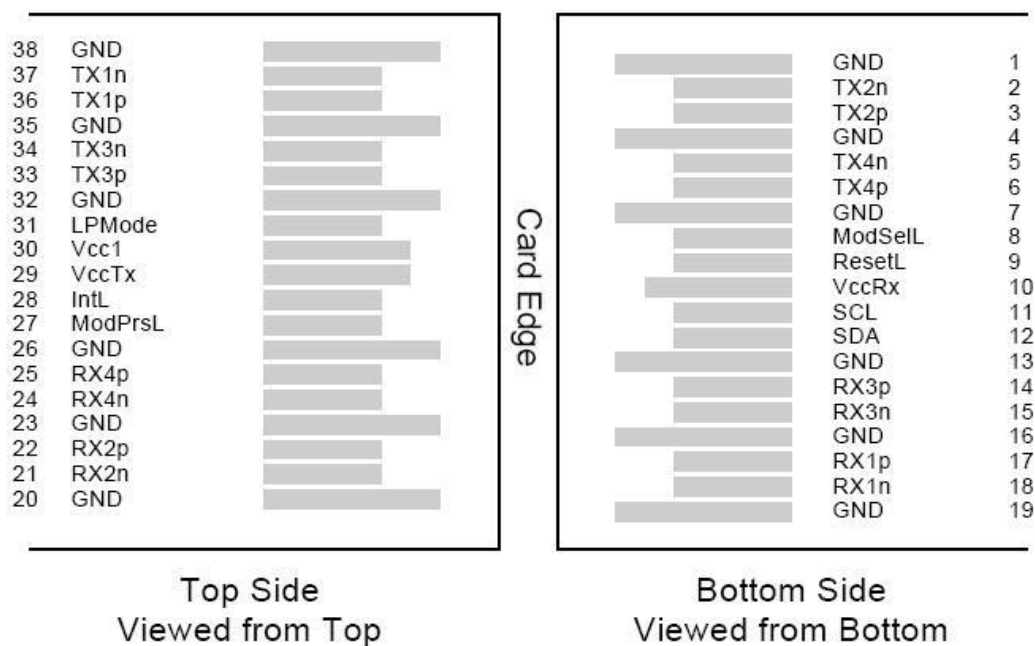
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Differential Return Loss (SDD22)	TP4			See CEI- 28G-VSR Equation 13- 19	dB	
Common Mode to Differential conversion and Differential to Common Mode conversion (SDC22, SCD22)	TP4			See CEI- 28G-VSR Equation 13- 21	dB	
Common Mode Return Loss (SCC22)	TP4			-2	dB	2
Transition Time, 20 to 80%	TP4	9.5			ps	
Vertical Eye Closure (VEC)	TP4			5.5	dB	
Eye Width at 10^{-15} probability (EW15)	TP4	0.57			UI	
Eye Height at 10^{-15} probability (EH15)	TP4	228			mV	


Notes:

1. Vcm is generated by the host. Specification includes effects of ground offset voltage.
2. From 250MHz to 30GHz.

8. Pin Descriptions




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PIN	Logic	Symbol	Name/Description	Note
1		GND	Ground	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	
3	CML-I	Tx2p	Transmitter Non-Inverted Data output	
4		GND	Ground	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	
6	CML-I	Tx4p	Transmitter Non-Inverted Data output	
7		GND	Ground	1
8	LVTLL-I	ModSelL	Module Select	
9	LVTLL-I	ResetL	Module Reset	
10		VccRx	+ 3.3V Power Supply Receiver	2
11	LVC MOS-I/O	SCL	2-Wire Serial Interface Clock	
12	LVC MOS-I/O	SDA	2-Wire Serial Interface Data	
13		GND	Ground	
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	
15	CML-O	Rx3n	Receiver Inverted Data Output	
16		GND	Ground	1
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	
18	CML-O	Rx1n	Receiver Inverted Data Output	
19		GND	Ground	1
20		GND	Ground	1
21	CML-O	Rx2n	Receiver Inverted Data Output	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	
23		GND	Ground	1
24	CML-O	Rx4n	Receiver Inverted Data Output	1
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	
26		GND	Ground	1
27	LVTTL-O	ModPrsL	Module Present	
28	LVTTL-O	IntL	Interrupt	
29		VccTx	+3.3 V Power Supply transmitter	2
30		Vcc1	+3.3 V Power Supply	2
31	LVTTL-I	LPMODE	Low Power Mode	
32		GND	Ground	1
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input	

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34	CML-I	Tx3n	Transmitter Inverted Data Output	
35		GND	Ground	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	
37	CML-I	Tx1n	Transmitter Inverted Data Output	
38		GND	Ground	1

Notes:

1. GND is the symbol for signal and supply (power) common for the QSFP28 module. All are common within the module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal common ground plane.

2. VccRx, Vcc1 and VccTx are the receiving and transmission power suppliers and shall be applied concurrently. Recommended host board power supply filtering is shown in Figure 2 below. Vcc Rx, Vcc1 and Vcc Tx may be internally connected within the module in any combination. The connector pins are each rated for a maximum current of 1000mA.

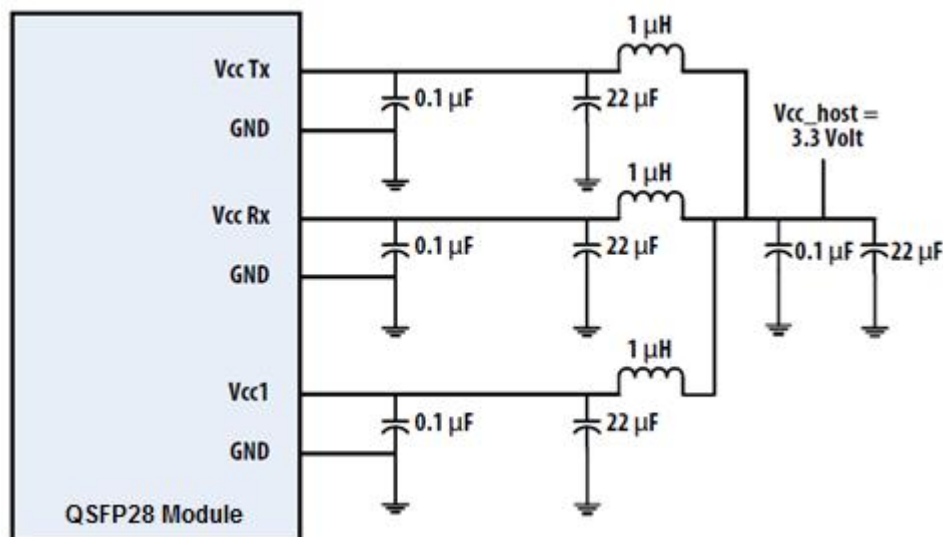


Figure 2 Power supply Filter


ModSelL Pin

The ModSelL is an input pin. When held low by the host, this product responds to 2-wire serial communication commands. The ModSelL allows the use of this product on a single 2-wire interface bus – individual ModSelL lines must be used.

Serial Clock (SCL) and Serial Data (SDA) are required for the 2-wire serial bus communication interface and enable the host to access the QSFP28 memory map.

ResetL Pin

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Reset. LPMode_Reset enables a complete reset, returning the settings to their default state, when a low level on the ResetL pin is held for longer than the minimum pulse length. During the execution of a reset the host shall disregard all status bits until it indicates a completion of the reset interrupt. The product indicates this by posting an IntL (Interrupt) signal with the Data_Not_Ready bit negated in the memory map. Note that on power up (including hot insertion) the module should post this completion of reset interrupt without requiring a reset.

LPMode Pin

LP Mode Pin is used to set the maximum power consumption for the product in order to protect hosts that are not capable of cooling higher power modules, should such modules be accidentally inserted.

ModPrsL Pin

ModPrsL is a signal local to the host board which, in the absence of a product, is normally pulled up to the host Vcc. When the product is inserted into the connector, it completes the path to ground through a resistor on the host board and asserts the signal. ModPrsL then indicates its present by setting ModPrsL to a “Low” state.

IntL Pin


IntL is an output pin. “Low” indicates a possible operational fault or a status critical to the host system. The host identifies the source of the interrupt using the 2-wire serial interface. The IntL pin is an open collector output and must be pulled to the Host Vcc voltage on the Host board.

9. Diagnostic Monitoring Interface

Digital diagnostics monitoring function is available on all OCRECOM’s OU8S341 QSFP28 LR4. A 2-wire serial interface provides user to contact with module. The structure of the memory is shown in the chart. The memory space is arranged into a lower, single page, address space of 128 bytes and multiple upper address space pages. This structure permits timely access to addresses in the lower page, such as Interrupt Flags and Monitors. Less time critical time entries, such as serial ID information and threshold settings, are available with the Page Select function. The interface address used is A0xh and is mainly used for time critical data like interrupt handling in order to enable a one-time-read for all data related to an interrupt situation. After an interrupt, IntL, has been asserted, the host can read out the flag field to determine the affected channel and type of flag.


Address	Size (Bytes)	Name	Description of Base ID Field	Optical Module
128	1	Identifier	Identifier Type of serial Module	
129	1	Ext. Identifier	Extended Identifier of Serial Module	
130	1	Connector	Code for connector type	
131-138	8	Specification compliance	Code for electronic compatibility or optical compatibility	

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139	1	Encoding	Code for serial encoding algorithm	
140	1	BR, nominal	Nominal bit rate, units of 100 Mbits/s	
141	1	Extended rateselect Compliance	Tags for extended rate select compliance	
142	1	Length(SMF)	Link length supported for SMF fiber in km (note 1)	
143	1	Length(OM3 50 um)	Link length supported for EBW 50/125 um fiber (OM3), units of 2m (note 1)	
144	1	Length(OM2 50 um)	Link length supported for 50/125 um fiber (OM2), units of 1m (note 1)	
145	1	Length(OM1 62.5 um)	Link length supported for 62.5/125 um fiber (OM1), units of 1m (note 1)	
146	1	Length (Copper)	Link length of copper or active cable, units of 1 m (note 1) Link length supported for 50/125 um fiber (OM4), units of 2 m) when Byte 147 declares 850nm VCSEL as defined in Table 37	
147	1	Device tech	Device technology	
148-163	16	Vendor name	QSFP28 vendor name(ASCII)	
164	1	Extended Module	Extended Module codes for InfiniBand	
165-167	3	Vendor OUI	QSFP28 vendor IEEE company ID	
168-183	16	Vendor PN	Part number provided by QSFP28 vendor(ASCII)	
184-185	2	Vendor rev	Revision level for part number provided by vendor(ASCII)	
186-187	2	Wave length or Copper cable Attenuation	Nominal laser wavelength (wavelength=value/20 in nm) or copper cable attenuation in dB at 2.5GH (Adrs 186) and 5.0GHz (Adrs 187)	
188-189	2	Wavelength tolerance	Guaranteed range of laser wavelength(+/- value) from nominal wavelength.(wavelength Tol.=value/200 in nm)	

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190	1	Max case temp.	Maximum case temperature in degrees C	
191	1	CC_BASE	Check code for base ID fields (addresses 128-190)	
192-195	4	Options	Rate Select, TX Disable, TX Fault, LOS, Warning indicators for: Temperature, VCC, RX, power, TX Bias	
196-211	16	Vendor SN	Serial number provided by vendor (ASCII)	
212-219	8	Date Code	Vendor's manufacturing date code	
220	1	Diagnostic Monitoring Type	Indicates which types of diagnostic monitoring are implemented (if any) in the Module. Bit 1,0 Reserved	
221	1	Enhanced Options	Indicates which optional enhanced features are implemented in the Module.	
222	1	Reserved		
223	1	CC_EXT	Check code for the Extended ID Fields (addresses 192-222)	
Vendor Specific ID Fields				
224-255	32	Vendor Specific EEPROM		

10. Digital Diagnostic Functions

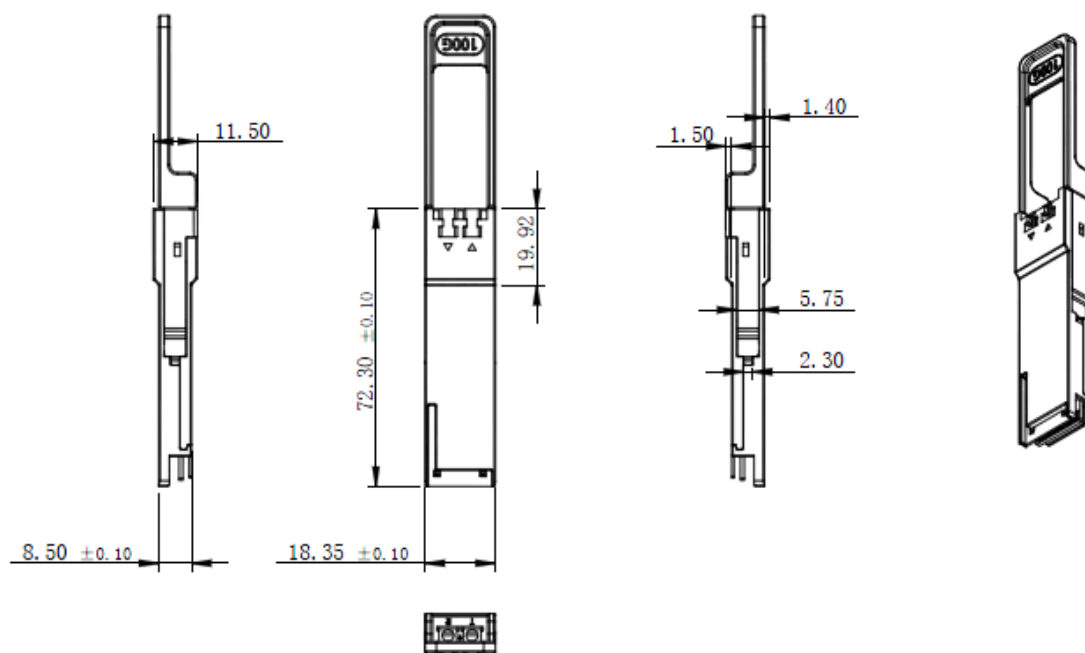
The following digital diagnostic characteristics are defined over the normal operating conditions unless otherwise specified.

Parameter	Symbol	Min	Max	Unit	Notes
Temperature monitor absolute error	DMI_Temp	-3	+3	degC	Over operating temp
Supply voltage monitor absolute error	DMI_VCC	-0.1	0.1	V	Full operating range
Channel RX power monitor absolute error	DMI_RX	-3	3	dB	Per channel
Channel Bias current monitor	DMI_Ibias	-10%	10%	mA	Per channel
Channel TX power monitor absolute error	DMI_TX	-3	3	dB	Per channel

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11. Mechanical Dimensions



ESD

This transceiver is specified as ESD threshold 1KV for high speed data pins and 2KV for all others electrical input pins, tested per MIL-STD-883, Method 3015.4 /JESD22-A114-A (HBM). However, normal ESD precautions are still required during the handling of this module. This transceiver is shipped in ESD protective packaging. It should be removed from the packaging and handled only in an ESD protected environment.

Laser Safety

This is a Class 1 Laser Product according to IEC 60825-1:2007. This product complies with 21 CFR 1040.10 and 1040.11 except for deviations pursuant to Laser Notice No. 50, dated (June 24, 2007)

12. Module Ordering information

PN	Description
OU8S341	100G QSFP28 LR4 1310nm-band 10KMLC DDMI 0~70 °C

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