



Datasheet

No.

DS10-E001

Initial Date

2010-07-29

OE

OE6M841

Written Team

R&D Dept.

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I Preview


PN	OE6M841
Description	SFP+ 850nm SR 300m

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III Revision History

No.	Date	Items	Change Recording	Ver.	Rev.	Customer
1	2010-07-29	All	Initial registration	000	000	Standard
2						
3						
4						
5						
6						

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1. Features:

- ◆ Hot pluggable
- ◆ 10Gb/s serial optical interface
- ◆ 850nm high-speed VCSEL and PIN receiver
- ◆ SFP+ MSA package with duplex LC connector
- ◆ 2-wire interface for management and diagnostic monitor
- ◆ SFI High Speed Electrical Interface
- ◆ Very low EMI and excellent ESD protection
- ◆ +3.3V single power supply
- ◆ Power consumption less than 0.8W
- ◆ Operating case temperature: 0~+70°C
- ◆ Compliant with SFF-8431 and SFF-8432
- ◆ Compliant with SFF-8472 Rev 10.2
- ◆ Compliant with IEEE 802.3ae 10GBASE-SR and 10GBASE-SW
- ◆ RoHS Compliant

2. Applications:

- ◆ 10G Base-SR/SW
- ◆ 10GE Storage, 8G Fiber Channel
- ◆ Other optical links


3. Description:

OE6M841 is a very compact 10Gb/s optical transceiver module for serial optical communication applications, supporting data-rate of 10.3125Gbps (10GBASE-SR) or 9.953Gbps (10GBASE-SW), and transmission distance up to 300m on 50μm MMF (2000MHz.km).

Fiber type	Minimum modal bandwidth @ 850 nm (MHz.km)	Operating range (meters)
62.5um MMF	160	2 to 26
	200	2 to 33
50um MMF	400	2 to 66
	500	2 to 82
	2000	2 to 300

SFP+ SR Operating Range for each Optical Fiber Type

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The transceiver consists of two sections: The high performance 850nm VCSEL transmitter and high Sensitivity PIN integrated with a TIA receiver.

4. Absolute Maximum Ratings

Parameter	Symbol	Remarks	Min.	Max.
Storage Temperature	T _{ST}	-40	+85	°C
Supply Voltage	V _{CC3}	0.0	+3.6	V
Relative Humidity	RH	5	95	%

5. Operation Environment:


Parameter	Symbol	Min	Typ	Max	Unit
Date Rate			10.3125		Gb/s
Supply Voltage	V _{CC}	+3.14	3.3	+3.47	V
Supply Current	I _{CC}			250	mA
Power Dissipation	PD		600	800	mW
Operating Temperature	T _{OP}	0	25	+70	°C

6. Optical Characteristics (Condition: T_a=T_{OP})

Parameter	Symbol	Min	Typ	Max	Unit	Ref.	
Transmitter							
Date Rate			10.3125		Gb/s		
Optical Wavelength	λ	840	850	860	nm		
Average output power	P _o	-6		-1	dBm	1	
Optical Extinction Ratio	ER	3			dB	1	
Laser Off Power	P _{off}	-	-	-30	dBm		
RMS spectral width	Δλ			0.45	nm		
Disabled Power	P _{off}	-		-30	dBm		
Dispersion penalty	TDP			3.9	dB		
Tx Jitter	T _{xj}	Per 802.3ae requirements					
Receiver							
Date Rate			10.3125		Gb/s		
Optical Wavelength	λ	840		860	nm		
Receiver Sensitivity(OMA)	R			-11	dBm	1	
Stressed Receiver Sensitivity(OMA)	R			-7.5	dBm	2	
Maximum Input Power	P _{MAX}	0			dBm		
LOS De-Assert	LOSD			-12	dBm		
LOS Assert	LOSA	-30			dBm		
LOS Hysteresis		0.5		4	dB		
Receiver Reflectance				-12	dB		

Notes:

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1) Measured at 10.3125b/s with PRBS $2^{31} - 1$ NRZ test pattern.

2) Under the ER worst case, measured at 10.3125 Gb/s with PRBS $2^{31} - 1$ NRZ test pattern for BER $< 1 \times 10^{-12}$

7. Electrical Characteristics (Condition: $T_a = T_{op}$)

Parameter	Symbol	Min	Typ	Max	Unit	Note
Transmitter:						
Differential input voltage swing	VI	90		350	mVpp	1
C common mode voltage tolerance		15	-	-	mV	
Transmit Disable Input	H	V_{IH}	2.0	$V_{cc}+0.3$	V	
	L	V_{IL}	0	0.8	V	
Transmit Enable Output	H	V_{OH}	2.4	$V_{cc}+0.3$	V	
	L	V_{OL}	0	0.4	V	2
Data Dependent Input Jitter	DDJ			0.1	UI	
Data Input Total Jitter	TJ			0.28	UI	
Input Differential Impedance	Z_{in}	80	100	120	Ω	
Receiver:						
Differential output voltage swing		300		850	mVpp	3
LOS Output	H	V_{OH}	2.4	$V_{cc}+0.3$	V	2
	L	V_{OL}	0	0.4	V	
Rx Output Rise and Fall Time	T_r/T_f	30			ps	20% to 80%
Total Jitter	TJ			0.7	UI	
Deterministic Jitter	DJ			0.42	UI	
Output Differential Impedance	Z_{on}	80	100	120	Ω	

Notes:

- 1) TD+/- are internally AC coupled with 100 Ω differential termination inside the module.
- 2) Tx Fault and Rx LOS are open collector outputs, which should be pulled up with 4.7k to 10k Ω resistors on the host board. Pull up voltage between 2.0V and $V_{cc}+0.3V$.
- 3) RD+/- outputs are internally AC coupled, and should be terminated with 100 Ω (differential) at the user SERDES.

8. Pin Information

Diagram of Host Board Connector Block Pin Numbers and Name

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OE serials

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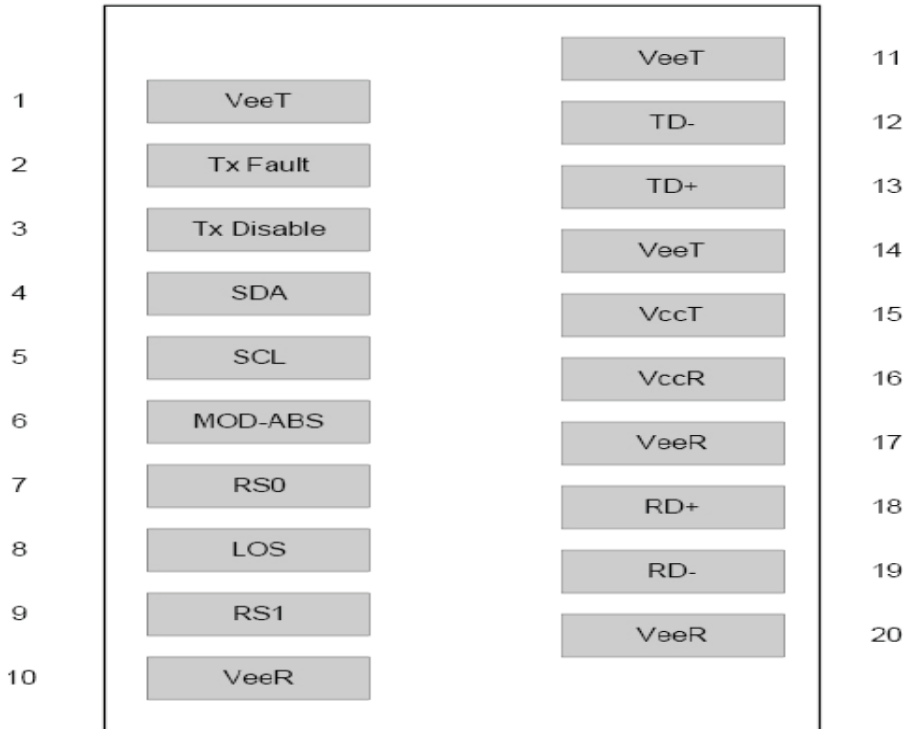
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PIN#	Name	Function	Notes
1	VeeT	Module transmitter ground	Note1
2	Tx Fault	Module transmitter fault	Note 2
3	Tx Disable	Transmitter Disable; Turns off transmitter laser output	Note 3
4	SDL	2 wire serial interface data input/output (SDA)	
5	SCL	2 wire serial interface clock input (SCL)	
6	MOD-ABS	Module Absent, connect to VeeR or VeeT in the module	Note 2
7	RS0	Receiver Rate Select	
8	LOS	Receiver Loss of Signal Indication	Note4
9	RS1	Transmitter Rate Select (not used)	
10	VeeR	Module receiver ground	Note 1
11	VeeR	Module receiver ground	Note 1
12	RD-	Receiver inverted data out put	
13	RD+	Receiver non-inverted data out put	
14	VeeR	Module receiver ground	Note 1
15	VccR	Module receiver 3.3V supply	
16	VccT	Module transmitter 3.3V supply	
17	VeeT	Module transmitter ground	Note 1
18	TD+	Transmitter inverted data out put	
19	TD-	Transmitter non-inverted data out put	
20	VeeT	Module transmitter ground	Note1

Notes:

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- 1) The module ground pins shall be isolated from the module case.
- 2) This pin is an open collector/drain output pin and shall be pulled up with 4.7K-10Kohms to Host_Vcc on the host board.
- 3) This pin shall be pulled up with 4.7K-10Kohms to VccT in the module.
- 4) This pin is an open collector/drain output pin and shall be pulled up with 4.7K-10Kohms to Host_Vcc on the host board.

9. SFP+ Module EEPROM information and management

The SFP+ modules implement the 2-wire serial communication protocol as defined in the SFP -8472.

The serial ID information of the SFP+ modules and Digital Diagnostic Monitor parameters can be accessed through the I2C interface at address A0h and A2h. The memory is mapped in Table 1. Detailed ID information (A0h) is listed in Table 2. And the DDM specification at address A2h. For more details of the memory map and byte definitions, please refer to the SFF-8472, "Digital Diagnostic Monitoring Interface for Optical Transceivers".

Table 1. Digital Diagnostic Memory Map (Specific Data Field Descriptions)

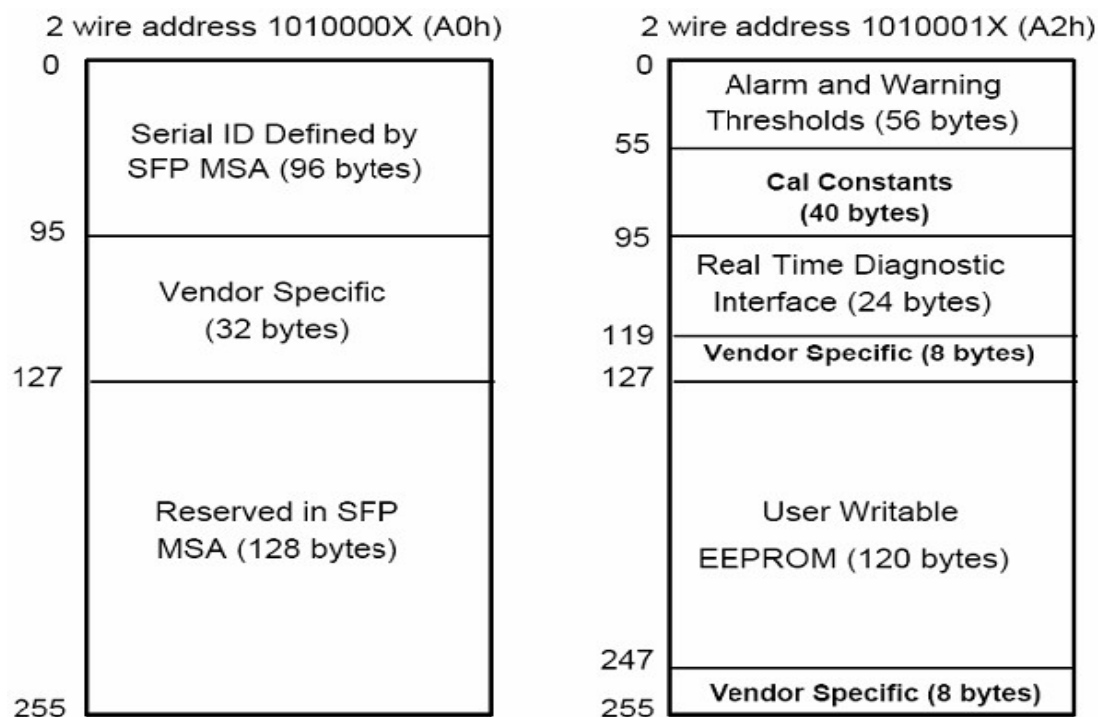



Table 2 - EEPROM Serial ID Memory Contents (A0h)

Data Address	Length (Byte)	Name of Length	Description and Contents
Base ID Fields			
0	1	Identifier	Type of Serial transceiver (03h=SFP)
1	1	Reserved	Extended identifier of type serial transceiver (04h)

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2	1	Connector	Code of optical connector type (07=LC)
3-10	8	Transceiver	
11	1	Encoding	64B/66B (06h)
12	1	BR,Nominal	Nominal baud rate, unit of 100Mbps (67h)
13-14	2	Reserved	(0000h)
15	1	Length(9um)	Link length supported for 9/125um fiber, units of 100m
16	1	Length(50um)	Link length supported for 50/125um fiber, units of 10m
17	1	Length(62.5um)	Link length supported for 62.5/125um fiber, units of 10m
18	1	Length(Copper)	Link length supported for copper, units of meters
19	1	Reserved	
20-35	16	Vendor Name	SFP vendor name: Ocrecom
36	1	Reserved	
37-39	3	Vendor OUI	SFP transceiver vendor OUI ID
40-55	16	Vendor PN	Part Number: "OE6M841" (ASCII)
56-59	4	Vendor rev	Revision level for part number
60-62	3	Reserved	
63	1	CCID	Least significant byte of sum of data in address 0-62
Extended ID Fields			
64-65	2	Option	Indicates which optical SFP signals are implemented (001Ah = LOS, TX_FAULT, TX_DISABLE all supported)
66	1	BR, max	Upper bit rate margin, units of %
67	1	BR, min	Lower bit rate margin, units of %
68-83	16	Vendor SN	Serial number (ASCII)
84-91	8	Date code	Ocrecom's Manufacturing date code
92	1	Diagnostic type	
93	1	Enhanced option	
94	1	SFF-8472	
95	1	CCEX	Check code for the extended ID Fields (addresses 64 to 94)
Vendor Specific ID Fields			
96-127	32	Readable	Ocrecom specific date, read only
128-255	128	Reserved	Reserved for SFF-8079

10. Digital Diagnostic Monitor Characteristics

Parameter	Symbol	Min.	Max	
Temperature monitor absolute error	DMI_Temp	-3	3	°C
Laser power monitor absolute error	DMI_TX	-3	3	dBm
RX power monitor absolute error	DMI_RX	-3	3	dBm
Supply voltage monitor absolute error	DMI_VCC	-0.08	0.08	V
Bias current monitor	DMI_Ibias	-10%	10%	mA

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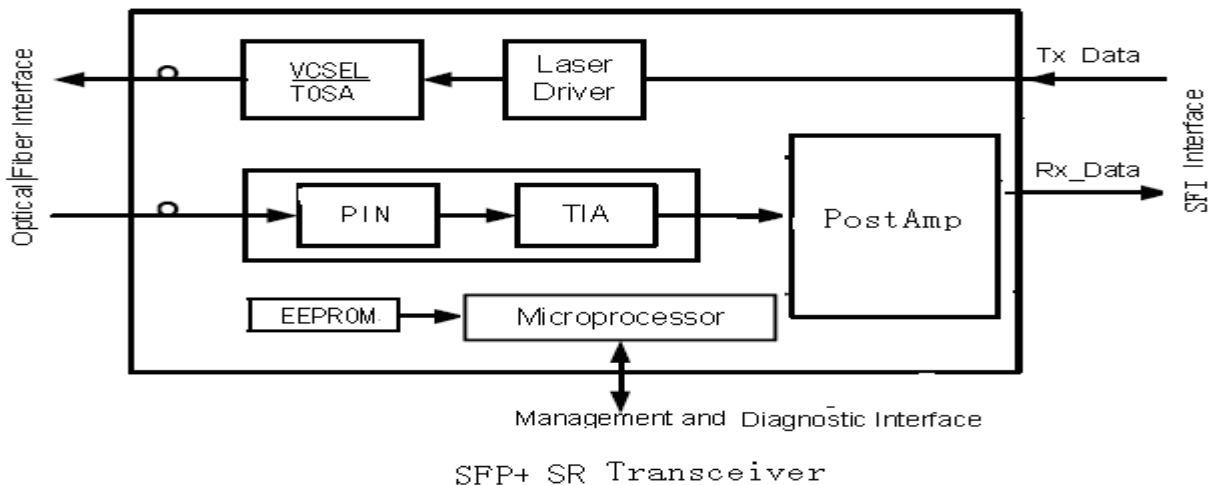
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11. Regular Compliance

The OE6M841 complies with international Electromagnetic Compatibility and International safety requirements and standards (see details in Table following).

Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883E Method 3015.4	Class 1(>2000 V)
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN55022 Class B (CISPR 22B) VCCI Class B	Compatible with standards
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN60950, EN (IEC) 60825-1,2	Compatible with Class 1 laser product.

12. A block diagram of the OE6M841 SFP+ optical transceiver is shown below



The module is hot pluggable into the 20-pin connector. The high-speed electrical interface is based on low voltage logic, with nominal 100 Ohms differential impedance and AC coupled in the module. The optical output can be disabled by LVTTTL logic high-level input of TX_Disable. Loss of signal (RX_LOS) output is provided to indicate the loss of an input optical signal of receiver.

2-wire interface (SCL, SDA) is used for serial ID, digital diagnostics and other control/monitor functions

13. Mechanical Dimensions:

Comply with SFF-8432 rev. 5.0, the improved Pluggable form factor specification.

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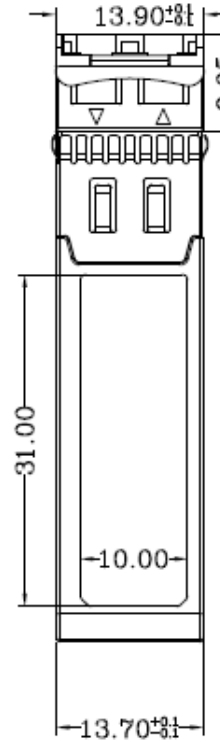
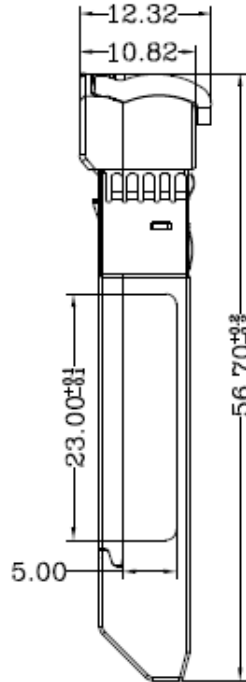
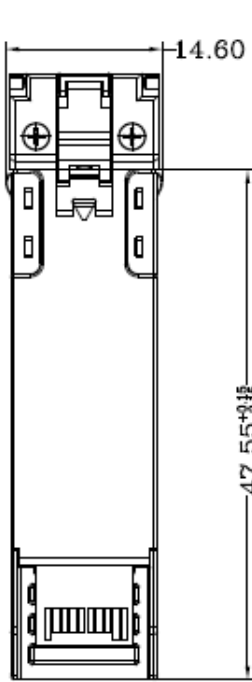
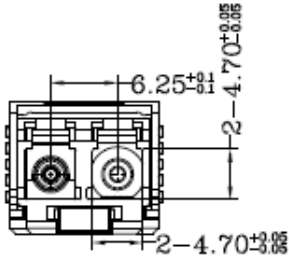


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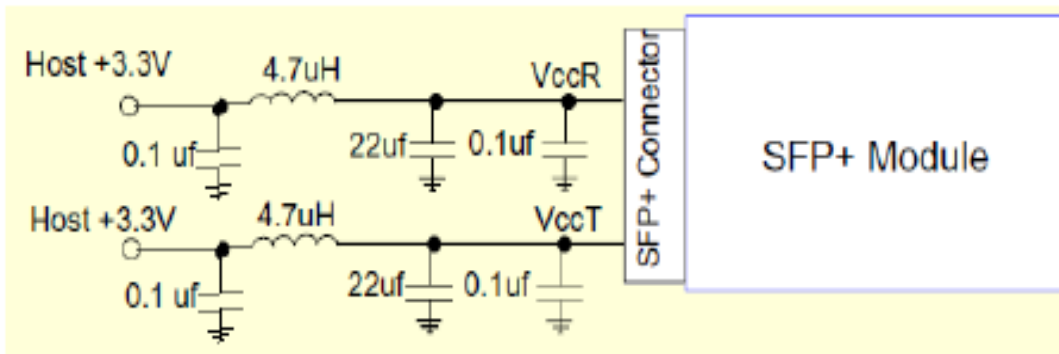
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14. Recommended Circuit:



Recommended Host Board Power Supply Circuit

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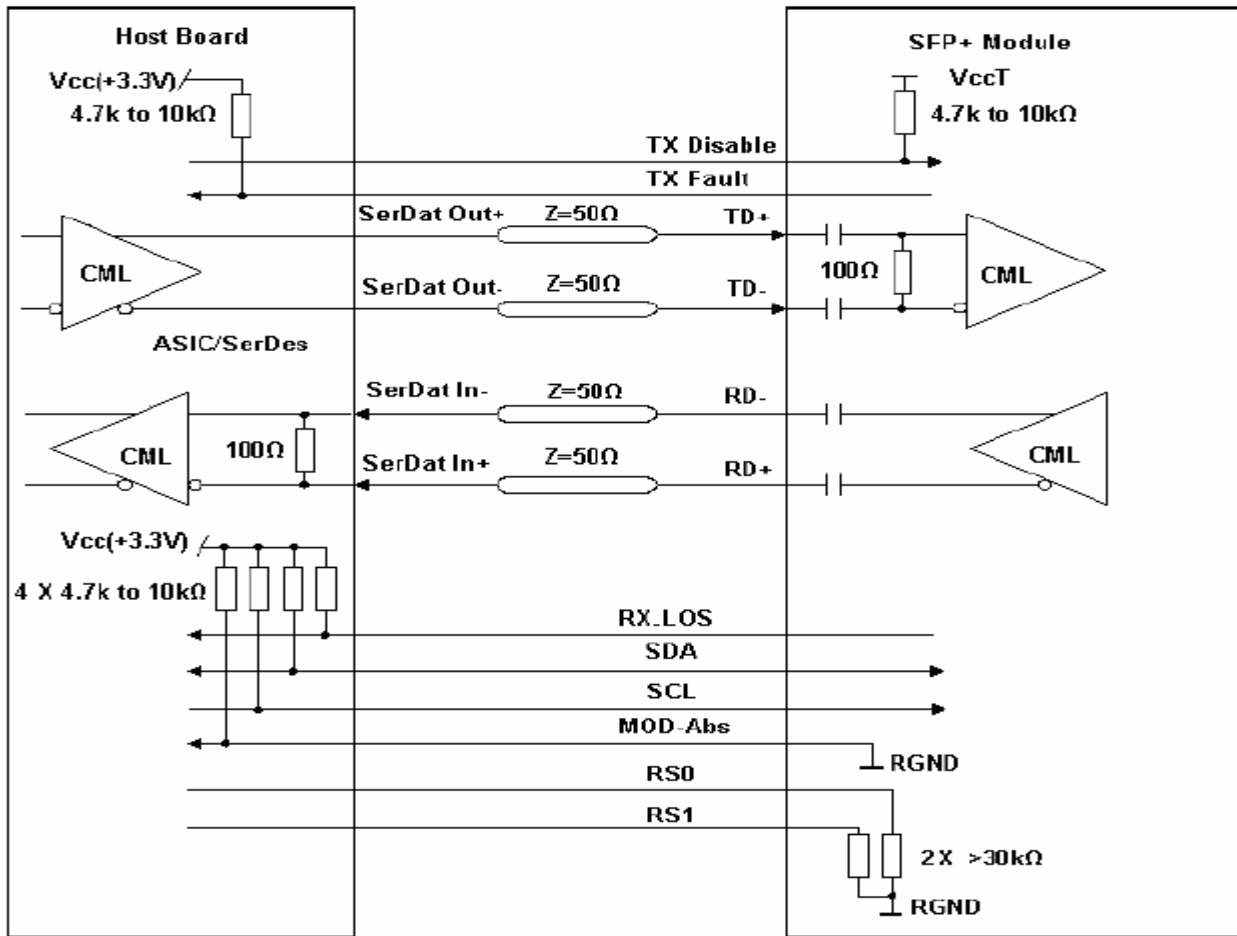
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Recommended High-speed Interface Circuit

15. Model Name Information

PN	Description
OE6M841	SFP+ SR 300m 0~70°C
OE6M842	SFP+ SR 300m -40~85°C

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